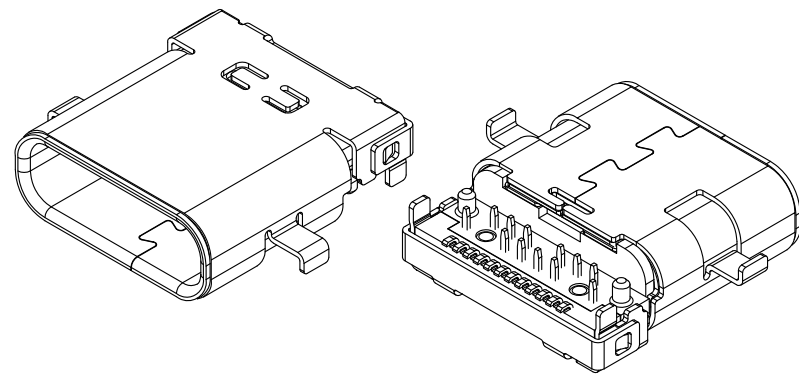
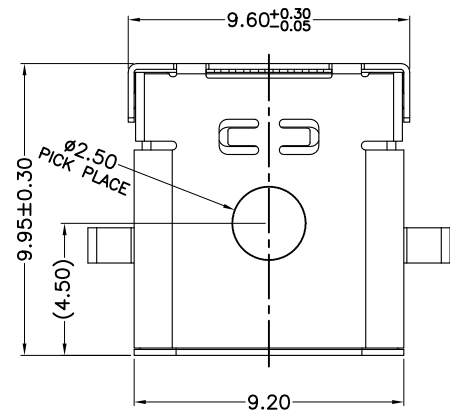
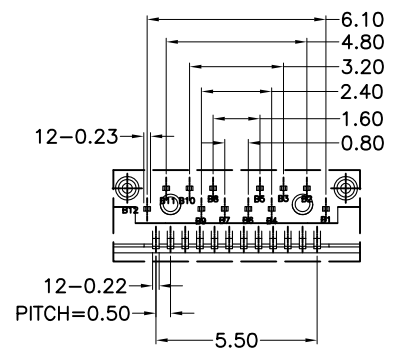
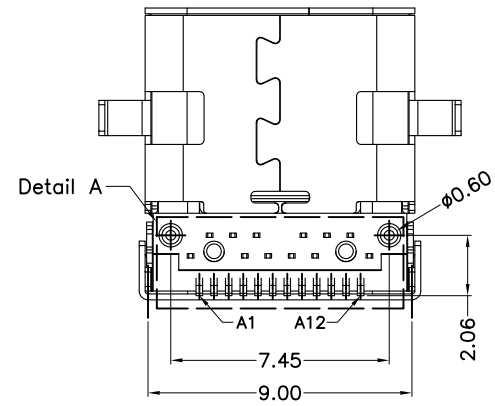
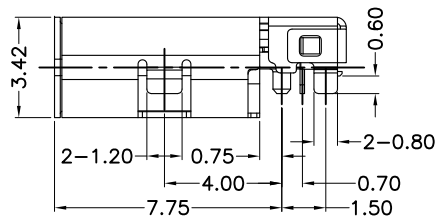
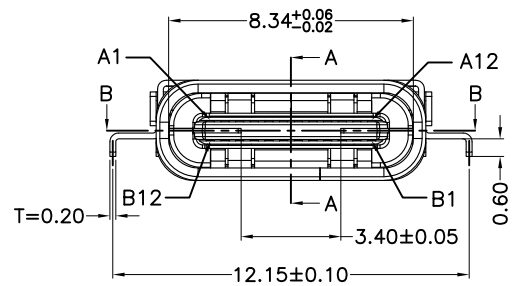


THE PART MEETS TECONN "GP001" COMPLIANCE NEW REV.



W1024P6-A-XX-R
Mating Area plating
01: Au 1u".
03: Au 3u"Min.
15: Au 15u"Min.
30: Au 30u"Min.

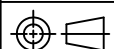


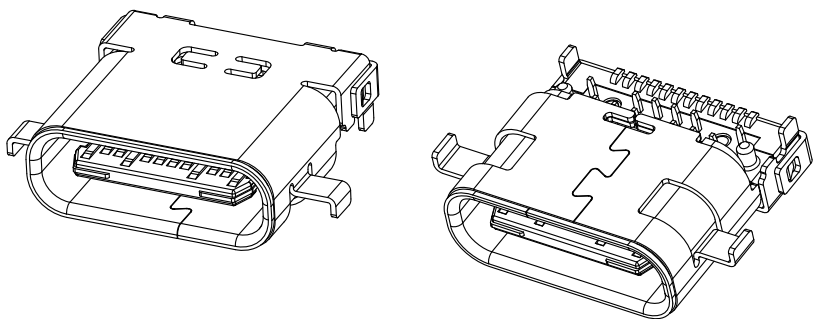
NOTES:

- 1.MECHANICAL:
INSERTION: 5~20N.
EXTRACTION: 8~20N.
DURABILITY: 10000 CYCLES
- 2.ELECTRICAL:
CURRENT: 5A MIN FOR VBUS;
0.25A MIN FOR OTHER.
VOLTAGE: 20 V MAX
WITHSTANDING VOLTAGE: 100V AC.
CONTACT RESISTANCE: 40mΩ MAX.
INSULATION RESISTANCE: 100MΩ MIN.
- 3.ENVIRONMENTAL
TEMPERATURE RANGE -55°C ~ +85°C

Item	Title	Material	Dispose	Q'TY
1	Terminal	Copper Alloy	Mating Area:See P/N Solder Tails:Au	24PCS
2	Housing	HIGH TEMPERATURE PLASTIC	Black,UL94V-0	1PCS
3	Shell	Stainless Steel	Plating Ni	1PCS
4	Shield	Stainless Steel	Clear Only	1PCS

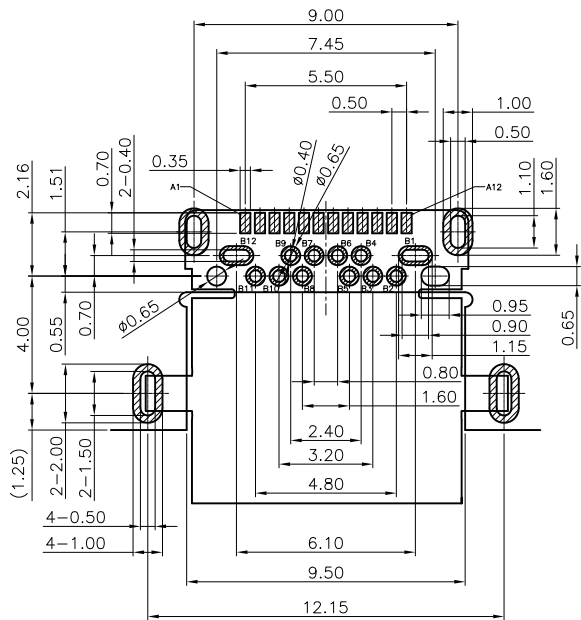
REV	ECN NO	DESCRIPTION	DESIGN	DATE
X1	TE2017-XXXX	New Drawing	XXXX	2017/07/25

X. ± 0.35 .X ± 0.25 .XX ± 0.15 .XXX±	APPD: Lebon Lee CHKD: /		TITLE: USB 3.1 C TYPE FEMALE SINK CONNECTOR CLO. 28 H2.00mm DIP TYPE	
X* ± 2* .X* ± 1.5*	DR: Drex			
	UNITS: mm	SCALE: 1:1	DWG NO: C-W1024P6-A-XX-R	CUSTOMER DRAWING
				SHEET:1OF2 REV: X1



USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	VBUS	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use(SBU)	B5	CC2	Configuraation Channel
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

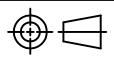


RECOMMEND P.C.B LAYOUT(COMPONENT SIDE)

TOLERANCE FOR PCB LAYOUT IS ± 0.05

KEEP OUT AREA

REV	ECN NO	DESCRIPTION	DESIGN	DATE
X1	TE2017-XXXX	New Drawing	XXXX	2017/07/25

X. ± 0.35	APPD: Lebon Lee	TITLE: USB 3.1 C TYPE FEMALE	
.X ± 0.25	CHKD: /	沉板双铁壳L=8.60 H=2.0	
.XX ± 0.15			
.XXX±	DR: Zhangjing	PART NO: W1024P6-B-XX-R	
X' ± 2'		DWG NO: C-W1024P6-B-XX-R	
.X' ± 1.5'		CUSTOMER DRAWING	
	UNITS: mm	SCALE: 1:1	SHEET:20F2
			REV: X1